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XA-9635
PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

Takeshi SAIKAWA et al.

Appln. No.:

Filed: Herewith

For: MANUFACTURING METHOD OF SEMICONDUCTOR DEVICE

11046 U.S. PTO
10/082311
02/26/02

* * *

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Pursuant to 37 C.F.R. § 1.56, and without any
assertion as to materiality or prior art effect, the
documents listed on the attached Form PTO-1449 are hereby
cited.

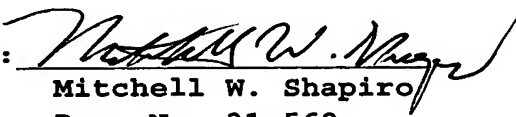
The documents listed on the attached List are cited in
the specification, on pages 2 and 3, and their relevance is
indicated therein.

Respectfully submitted,

MWS:lmb

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By:


Mitchell W. Shapiro
Reg. No. 31,568

February 26, 2002